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JUL 23 2004

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20350 7590 04/22/2004

TOWNSEND AND TOWNSEND AND CREW, LLP
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|---------------|--------------------|
| Gloria Sikora | (Depositor's name) |
| <i>Gloria</i> | (Signature) |
| July 19, 2004 | (Date) |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/994,545 | 11/26/2001 | Peter Rabkin | 00939A-085300US | 8177 |

TITLE OF INVENTION: POLYSILICON LAYERS STRUCTURE AND METHOD OF FORMING SAME

NON-VOLATILE MEMORY CELL WITH MULTI-POLYSILICON LAYER GATES (as amended)

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|-------------|--------------|-----------|-----------------|------------------|----------|
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| nonprovisional | NO | \$1330 | \$300 | \$1630 | 07/22/2004 |
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| EXAMINER | ART UNIT | CLASS-SUBCLASS |
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| IM, JUNGHWA M | 2811 | 257-315000 |
|---------------|------|------------|

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hynix Semiconductor, Inc.

Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): individual corporation or other private group entity government

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(Authorized Signature) *Gloria Sikora* (Date) July 8, 2004
Gloria Sikora, Reg # 45,018

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| 03 FC:8001 30.00 DA |

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